

# SHARP SERVICE MANUAL

No. 00ZFO8MKU/SME

## FACSIMILE OPTION MEMORY UPGRADE PWB

MODEL **FO-8MK**

This machine is an Option Memory Unit applicable to the FO-4400 Series.

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## [1] General description

This machine is an Option Memory Unit applicable to the FO-4400 Series. It enhances the memory-aided functions such as sequential simultaneous sending and through-memory substitute reception of facsimile.

## [2] Installation procedure (Pay attention to static electricity)

To install the Option Memory Unit, check the following in advance.

- 1) Make sure that the remaining memory capacity is 0%.  
If the remaining memory capacity is not 0%, this implies that any data remains in the image memory on the Control PWB.
- 2) Check that a sufficient quantity of paper exists. If there is no paper or paper is lacking, replenish the machine with paper, and print the data received and stored in the memory.
- 3) Then, check whether there is any confidentially received information, seeing the confidential reception data list ("MENU" + "2" + "0" + "9").  
If any confidentially received information is found, input the ID No. of specific reception box, and print the confidentially received information.
- 4) After that, check whether there is timer sending information, seeing the timer sending information list ("MENU" + "2" + "0" + "2"). It is required to teach users that any timer sending information, if exists, is cleared after the Option Memory Unit is installed and, accordingly, users have to set again the timer sending and do the timer sending.

Install the Option Memory Unit in the following procedure.

- 1) Remove the Rear Cabinet of facsimile.
- 2) Loose two screws on the Standard Memory PWB and remove two spacers. Then, remove the Standard Memory PWB from two locking spacers and the connector.
- 3) Connect the Option Memory PWB to the connector CNOP1 of Control PWB and insert two locking spacers into the holes on the board.  
Then, tighten two screws with each spacers. (Fig. 1)
- 4) Mount the Rear Cabinet.
- 5) After installation turn on power switch with pressing "START" + "STOP" key.

```

M E M O R Y   C L E A R ?
1 = A L L ,   2 = I M A G E ,   3 = N O

```

appears.

Then, press "1" key to perform "ALL" clear.

```

P L E A S E   W A I T   A   M O M E N T

```

appears.

Then, after several seconds,

```

S E P - 2 6   T U E   1 0 : 4 7 A M
M : 0 0 %   S T A N D - B Y   A U T O

```

appears.

- The data initially registered will be deleted by this operation.

## [3] Diagnostic mode

Successively input "MENU" + "9" + "x" + "8" + "#" + "7". Then, appears.

```

M A I N : T A 6 8 *

```

(\* is ROM version.)

After that, press the "START" key.

Then, appears.

```

A : S O F T   S W I T C H   M O D E
P R E S S   S T A R T   K E Y

```

Select the Flash Memory Test, using the key "L".

```

L : F L A S H   M E M O R Y
P R E S S   S T A R T   K E Y

```

In the diagnostic mode perform the Image Memory (Standard, Option) write/read test, as well as complete erase test.

When the "START" key is pressed,

```

S - - E
T E S T   E X E C U T I N G

```

appears, and the indication changes from "-" to "■" whenever data is written in the Flash Memory.

After completion of writing of data in all Flash Memories, the following indication appears.

```

S ■ ■ E
T E S T   E X E C U T I N G

```

After that the long-tone buzzer (normal end) sounds, and at the same time the following indication appears.

```

M : A L L   F A X / T E L   E N T R Y
P R E S S   S T A R T   K E Y

```

Then, the result table (Fig. 2) is printed.

In case of mismatch, the buzzer emits 3 long tones.

After that, the result table (Fig. 3) or (Fig.4) is output.

- 1) If any error occurred, check connection of connector CNOP1 (check for bending and breakage of pin of connector), check that the Option Memory Unit has been mounted as specified, and then turn on again the power supply, and check.
- 2) If the same error occurs persistently after the measures stated in item 1 above were taken, replace the Option Memory Unit, and check.
- 3) If the same error occurs again after the measures stated in item 2) were taken, check the Control PWB.

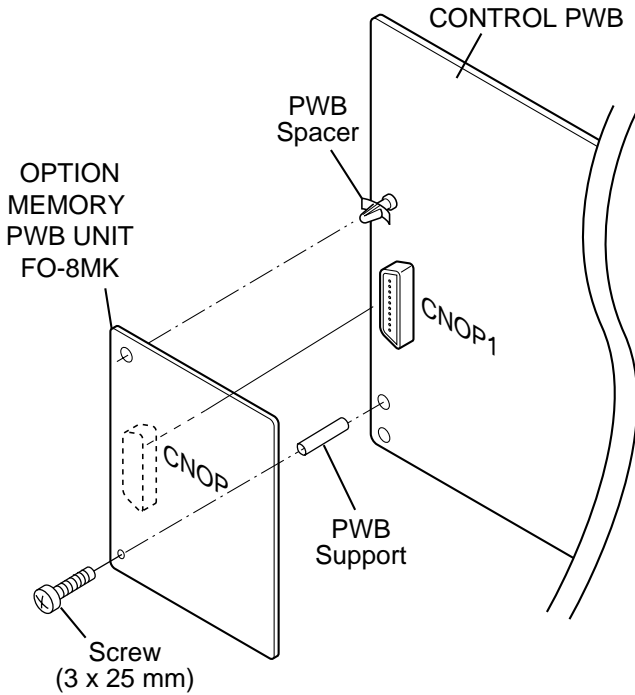


Fig. 1

```

XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
X                                     FLASH MEMORY CHECK LIST                               X
X                                                                                   OCT-08-2002 TUE 06:41 PM                    X
X-----X
X NOR-FLASH = OK                                                                    X
X NAND-FLASH STANDARD = OK [ 2MB]                                                  X
X OPTION = OK [ 8MB]                                                                X
X-----X
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
    
```

Fig. 2

```

XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
X                                     FLASH MEMORY CHECK LIST                               X
X                                                                                   OCT-08-2002 TUE 06:36 PM                    X
X-----X
X NOR-FLASH = OK                                                                    X
X NAND-FLASH STANDARD = OK [ 2MB]                                                  X
X-----X
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
    
```

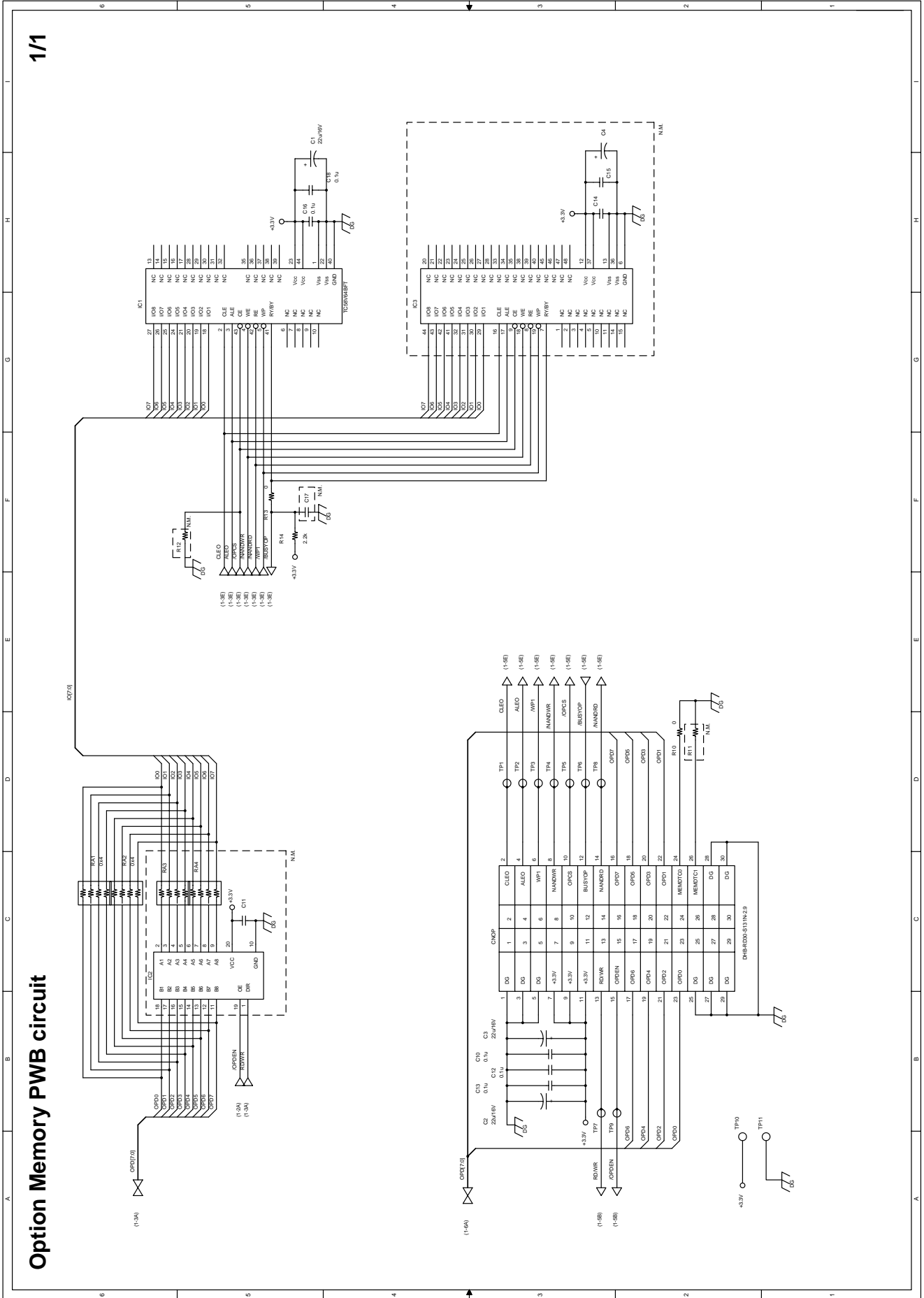
Fig. 3

```

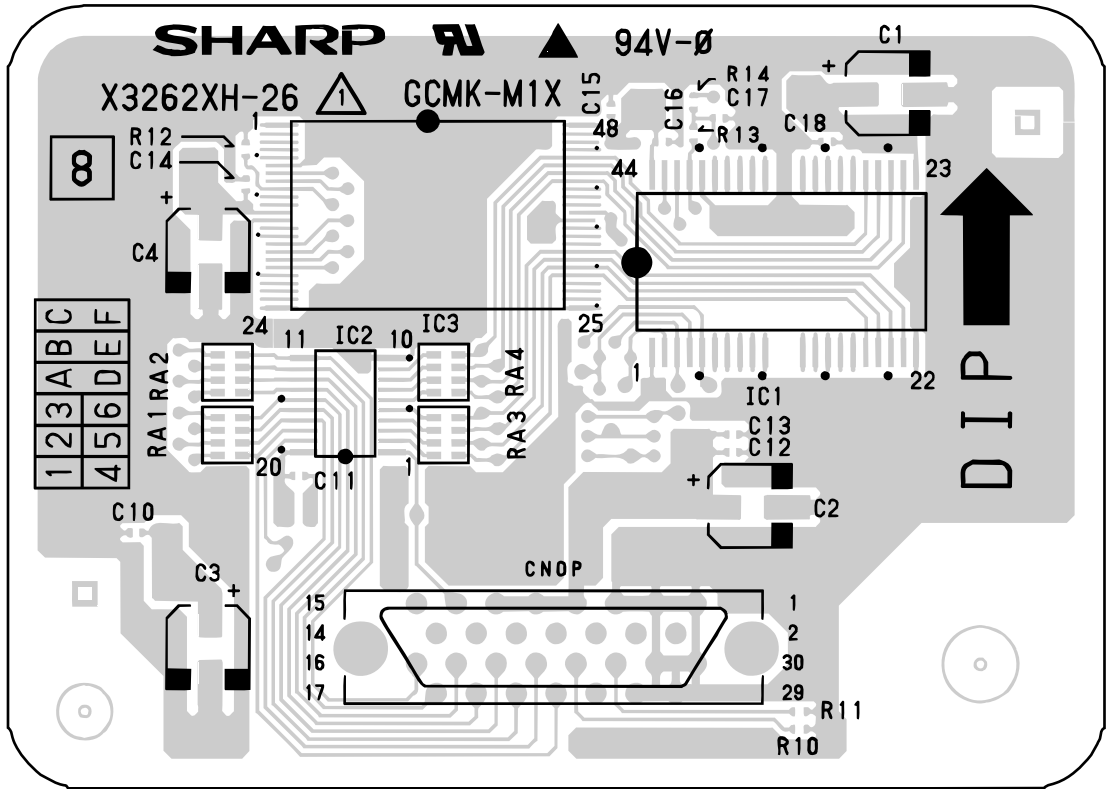
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
X                                     FLASH MEMORY CHECK LIST                               X
X                                                                                   OCT-08-2002 TUE 07:25 PM                    X
X-----X
X NOR-FLASH = OK                                                                    X
X NAND-FLASH STANDARD = OK [ 2MB]                                                  X
X OPTION = *** NG *** [ 8MB]                                                       X
X-----X
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
    
```

Fig. 4

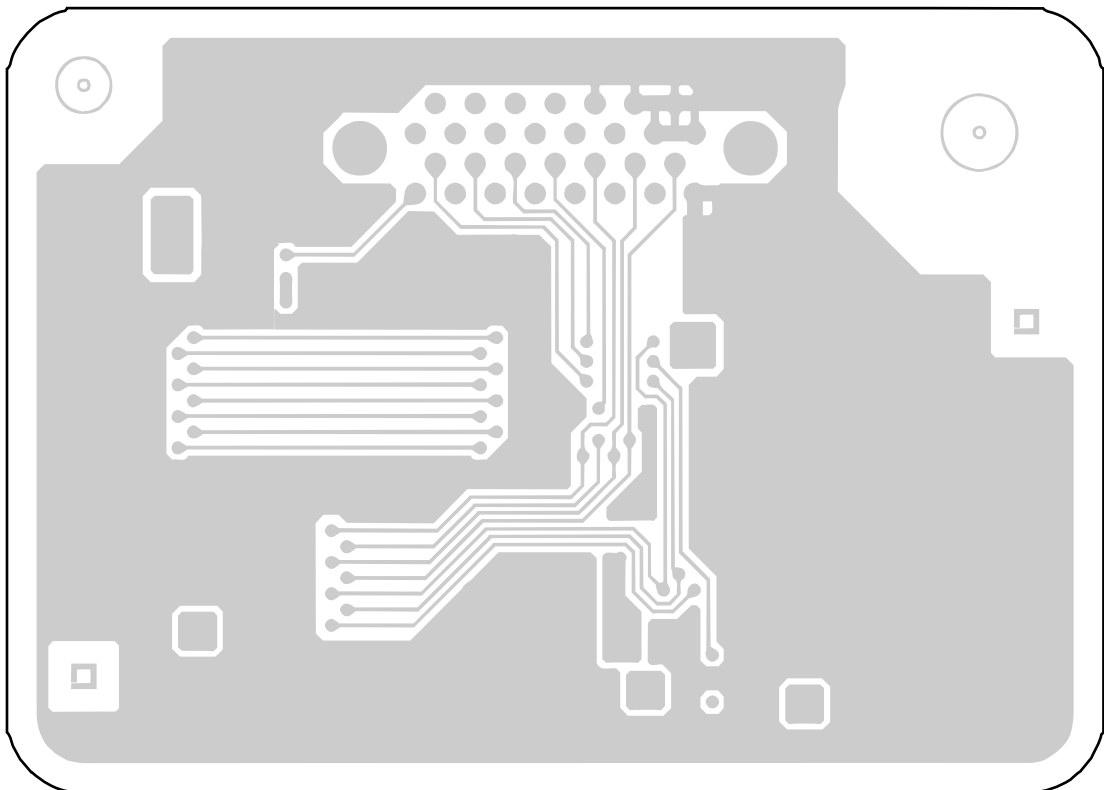
[4] Circuit schematics and parts layout



Option Memory PWB parts layout (Top side)



Option Memory PWB parts layout (Bottom side)



# SHARP PARTS GUIDE

MODEL **FO-8MK**

## CONTENTS

- 1 Packing Parts
- 2 Option Memory PWB Unit

NO.	PARTS CODE	PRICE RANK	NEW MARK	PART RANK	DESCRIPTION
[1] Packing parts					
1	LSTY-0057AFZZ	AC		C	PWB spacer
2	PSHEM3490XHZZ	AC		C	Aluminum fold
3	SPAKC210EXHTZ	AE	N	D	Packing case
4	PCAPZ2030XHZZ	AF		C	PWB support
5	XHP3SD30P25000	AA		C	Screw(3x25)
[2] Option memory PWB unit					
1	VCEAPS226AF1C	AC		C	Capacitor(16WV 22 $\mu$ F) [C1]
2	VCEAPS226AF1C	AC		C	Capacitor(16WV 22 $\mu$ F) [C2]
3	VCEAPS226AF1C	AC		C	Capacitor(16WV 22 $\mu$ F) [C3]
4	VCKYCZ1CF104Z	AB		C	Capacitor(16WV 0.1 $\mu$ F) [C10]
5	VCKYCZ1CF104Z	AB		C	Capacitor(16WV 0.1 $\mu$ F) [C12]
6	VCKYCZ1CF104Z	AB		C	Capacitor(16WV 0.1 $\mu$ F) [C13]
7	VCKYCZ1CF104Z	AB		C	Capacitor(16WV 0.1 $\mu$ F) [C16]
8	VCKYCZ1CF104Z	AB		C	Capacitor(16WV 0.1 $\mu$ F) [C18]
9	QCNCW2590SC3J	AS	N	C	Connector(30pin) [CNOP]
10	RH-IX2298XHZZ	BK	N	B	IC(TC58V64BFT) [IC1]
11	VRS-CZ1JB000J	AA		C	Resistor(1/16W 0 $\Omega$ $\pm$ 5%) [R10]
12	VRS-CZ1JB000J	AA		C	Resistor(1/16W 0 $\Omega$ $\pm$ 5%) [R13]
13	VRS-CZ1JB222J	AD		C	Resistor(1/16W 2.2K $\Omega$ $\pm$ 5%) [R14]
14	RR-TZ3021SCZZ	AF		B	Block resistor(0 $\Omega$ x4) [RA1]
15	RR-TZ3021SCZZ (Unit)	AF		B	Block resistor(0 $\Omega$ x4) [RA2]
901	DCEKM433CXH01	BA	N	E	Option memory PWB unit

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